

EAST Search History

| Ref # | Hits | Search Query | DBs | Default Operator | Plurals | Time Stamp |
|-------|------|---|--------------------|------------------|---------|------------------|
| L2 | 2 | "virtual processor is" | US-PGPUB; USPAT | OR | ON | 2006/03/15 09:24 |
| L3 | 18 | "virtual processor" with (advantag\$3 benefi\$4) | US-PGPUB; USPAT | OR | ON | 2006/03/15 09:28 |
| L4 | 1 | "6167501".pn. | US-PGPUB; USPAT | OR | ON | 2006/03/15 09:28 |
| S1 | 28 | bit with (first near2 configur\$5) with (second near2 configur\$5) and "712"/\$3?.ccls. | US-PGPUB; USPAT | OR | ON | 2004/06/24 09:30 |
| S2 | 7 | array with (first near2 configur\$5) with (second near2 configur\$5) and "712"/\$3?.ccls. | US-PGPUB; USPAT | OR | ON | 2004/06/23 16:29 |
| S3 | 19 | parady.in. and g06f\$.ipc. | US-PGPUB; USPAT | OR | ON | 2004/06/23 16:38 |
| S4 | 1 | "4763242".pn. | US-PGPUB; USPAT | OR | ON | 2004/06/23 16:38 |
| S5 | 1 | bit with (first near2 context) with (second near2 context) and "712"/\$3?.ccls. | US-PGPUB; USPAT | OR | ON | 2004/06/24 09:38 |
| S6 | 154 | bit with reconfigur\$5 and "712"/\$3?. ccls. | US-PGPUB; USPAT | OR | ON | 2004/06/24 10:19 |
| S7 | 1 | "5890008".pn. | US-PGPUB; USPAT | OR | ON | 2004/06/24 10:22 |
| S8 | 317 | (status state) near2 register with (sleep nap idle) and g06f\$.ipc. | US-PGPUB; USPAT | OR | ON | 2004/06/24 10:23 |
| S9 | 47 | (status state) near2 register with processor with (sleep nap idle) and g06f\$.ipc. | US-PGPUB; USPAT | OR | ON | 2004/06/24 13:21 |
| S10 | 4 | (mode) near2 register with processor with (sleep nap idle) and g06f\$.ipc. | US-PGPUB; USPAT | OR | ON | 2004/06/24 10:44 |
| S11 | 11 | (status state) near2 register with processor with (sleep nap idle) with (active busy) and g06f\$.ipc. | US-PGPUB; USPAT | OR | ON | 2004/06/24 16:05 |
| S12 | 29 | VLIW with thread and g06f\$.ipc. | US-PGPUB; USPAT | OR | ON | 2004/06/25 10:16 |
| S13 | 130 | 712/10.ccls. | US-PGPUB; USPAT | OR | OFF | 2004/06/25 10:26 |
| S14 | 324 | 712/11.ccls. | US-PGPUB; USPAT | OR | OFF | 2004/06/25 10:26 |
| S15 | 177 | 712/15.ccls. | US-PGPUB; USPAT | OR | OFF | 2004/06/25 10:27 |

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| S16 | 446 | 712/228.ccls. | US-PGPUB; USPAT | OR | OFF | 2004/06/25 10:30 |
| S17 | 2 | (multiplexer selector) with (plurality multiple) with (register adj (file set)) same context | US-PGPUB; USPAT | OR | ON | 2004/06/25 10:33 |
| S18 | 45 | (multiplexer selector) with (register adj (file set)) same context | US-PGPUB; USPAT | OR | ON | 2004/06/25 10:34 |
| S19 | 145 | 712/10.ccls. | US-PGPUB; USPAT | OR | OFF | 2004/11/17 09:56 |
| S20 | 332 | 712/11.ccls. | US-PGPUB; USPAT | OR | OFF | 2004/11/17 09:56 |
| S21 | 186 | 712/15.ccls. | US-PGPUB; USPAT | OR | OFF | 2004/11/17 09:56 |
| S22 | 482 | 712/228.ccls. | US-PGPUB; USPAT | OR | OFF | 2004/11/17 09:56 |
| S23 | 39 | bit with (first near2 configur\$5) with (second near2 configur\$5) and "712"/\$3?.ccls. | US-PGPUB; USPAT | OR | ON | 2006/03/10 13:05 |
| S24 | 240 | (array matrix) same (first near2 configur\$5) same (second near2 configur\$5) same process\$3 | US-PGPUB; USPAT | OR | ON | 2006/03/10 13:06 |
| S25 | 18 | (array matrix) same (first near2 configur\$5) same (second near2 configur\$5) same process\$3 same (row and column) | US-PGPUB; USPAT | OR | ON | 2006/03/10 13:09 |
| S26 | 14 | (array matrix) same (first near2 configur\$5) same (second near2 configur\$5) same process\$3 and "712"/\$.ccls. | US-PGPUB; USPAT | OR | ON | 2006/03/10 13:14 |
| S27 | 1 | (array matrix) same (first near2 configur\$5) same (second near2 configur\$5) same (row column) and "712"/\$.ccls. | US-PGPUB; USPAT | OR | ON | 2006/03/10 13:15 |
| S28 | 0 | (array matrix) same \$2configur\$5 with different with number with (row column) and "712"/\$.ccls. | US-PGPUB; USPAT | OR | ON | 2006/03/10 13:15 |
| S29 | 3 | (array matrix) same \$2configur\$5 with different with (row column) and "712"/\$.ccls. | US-PGPUB; USPAT | OR | ON | 2006/03/10 13:20 |
| S30 | 1 | (array matrix) same \$2configur\$5 with different\$2 with organiz\$5 and "712"/\$.ccls. | US-PGPUB; USPAT | OR | ON | 2006/03/10 13:21 |
| S31 | 23 | (array matrix) with \$2configur\$5 with organiz\$5 and "712"/\$.ccls. | US-PGPUB; USPAT | OR | ON | 2006/03/13 08:36 |
| S32 | 0 | (array matrix) same (("3x3" "3 x 3" "3 by 3") and ("2 x 2" "2 by 2" "2x2")) and "712"/\$.ccls. | US-PGPUB; USPAT | OR | ON | 2006/03/13 08:38 |

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| S33 | 12 | (array matrix) same reconfigur\$5 same (flag bit) near4 (set setting) and "712"/\$.ccls. | US-PGPUB; USPAT | OR | ON | 2006/03/13 09:20 |
| S34 | 9 | (li gifford).in. and SIMD and "712"/\$.ccls. | US-PGPUB; USPAT | OR | ON | 2006/03/13 09:25 |
| S35 | 39 | bit with (enable disable) with (process\$3 near3 element) and "712"/\$.ccls. | US-PGPUB; USPAT | OR | ON | 2006/03/13 10:10 |
| S36 | 240 | (array matrix) and (1x? ?x1) and "712"/\$.ccls. | US-PGPUB; USPAT | OR | ON | 2006/03/13 10:10 |
| S37 | 13 | (array matrix) same (1x? ?x1) same (disabl\$3 enabl\$3) and "712"/\$.ccls. | US-PGPUB; USPAT | OR | ON | 2006/03/13 10:11 |
| S38 | 0 | (array matrix) same ("1 x ?" "? x 1") same (disabl\$3 enabl\$3) and "712"/\$.ccls. | US-PGPUB; USPAT | OR | ON | 2006/03/13 10:18 |
| S39 | 0 | (array matrix) near4 (1x? ?x1) and "712"/\$.ccls. | US-PGPUB; USPAT | OR | ON | 2006/03/13 10:12 |
| S40 | 580 | bit with (disabl\$3 enabl\$3) with (processor coprocessor) and "712"/\$.ccls. | US-PGPUB; USPAT | OR | ON | 2006/03/13 10:20 |
| S41 | 67 | bit near3 (disabl\$3 enabl\$3) near3 ((process\$3 near3 element) PE) and "712"/\$.ccls. | US-PGPUB; USPAT | OR | ON | 2006/03/13 15:21 |
| S42 | 7 | (US-4783738-\$ or US-5045995-\$ or US-4873626-\$ or US-4891787-\$ or US-5243699-\$ or US-5542074-\$ or US-5727229-\$).did. | USPAT | OR | ON | 2006/03/13 11:38 |
| S43 | 0 | (US-4783738-\$ or US-5045995-\$ or US-4873626-\$ or US-4891787-\$ or US-5243699-\$ or US-5542074-\$ or US-5727229-\$).did. and "register file" with each | USPAT | OR | ON | 2006/03/13 11:39 |
| S44 | 2 | (US-4783738-\$ or US-5045995-\$ or US-4873626-\$ or US-4891787-\$ or US-5243699-\$ or US-5542074-\$ or US-5727229-\$).did. and "register file" | USPAT | OR | ON | 2006/03/13 11:39 |
| S45 | 1 | "4314349".pn. | US-PGPUB; USPAT | OR | ON | 2006/03/14 10:19 |
| S46 | 737 | (process task thread) same (ID identifier) same (physical and virtual) | US-PGPUB; USPAT | OR | ON | 2006/03/14 10:32 |
| S47 | 539 | (process task thread) same (ID identifier) same (physical and virtual) and g06f\$.ipc. | US-PGPUB; USPAT | OR | ON | 2006/03/14 10:32 |

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| S48 | 88 | (process task thread) same (ID identifier) same (physical and virtual) and (PE (process\$3 near2 element)) and g06f\$.ipc. | US-PGPUB; USPAT | OR | ON | 2006/03/14 10:34 |
| S49 | 16 | (process task thread) same (ID identifier) near4 (physical and virtual) and (PE (process\$3 near2 element)) and g06f\$.ipc. | US-PGPUB; USPAT | OR | ON | 2006/03/14 16:39 |
| S50 | 92 | vliw with simd with (type form) | US-PGPUB; USPAT | OR | ON | 2006/03/14 16:31 |
| S51 | 728 | instruction with virtual with (ID identifi\$6) | US-PGPUB; USPAT | OR | ON | 2006/03/14 16:32 |
| S52 | 284 | instruction with virtual near2 (ID identifi\$6) | US-PGPUB; USPAT | OR | ON | 2006/03/14 16:32 |
| S53 | 87 | instruction near3 virtual near2 (ID identifi\$6) | US-PGPUB; USPAT | OR | ON | 2006/03/14 16:32 |
| S54 | 21 | (ID identifier) with (physical and virtual) same (PE (process\$3 near2 element)) and g06f\$.ipc. | US-PGPUB; USPAT | OR | ON | 2006/03/15 09:23 |